AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions, and listing of claims in the application:

LISTING OF CLAIMS:

Claim 1 (Current amendment) A pixel for an in-plane switching liquid crystal display, comprising:

a first structure on a substrate having a reflective surface with a nanometer scale roughness defined by a height in the range of 5-50 nm and a width less than 20 nm for enhancing a light scattering angle and contrast enhancement, the first structure including (a) a micro scattering layer on the substrate with the nanometer scale roughness, the micro scattering layer being formed by a conductor on the substrate and an insulator on the conductor with the nanometer scale roughness and (b) a reflective layer made of high reflective metal on the micro scattering layer and conformal to the micro scattering layer to thereby form the reflective surface;

a second structure on a first part of the first structure having a switch device thereof;

a liquid crystal layer above the second structure and a second part of the first structure; and

a third structure on the liquid crystal layer.

Claims 2 - 4 (Cancelled).

Claim 5 (Original) The pixel of claim 1, wherein the second part comprises a plurality of reflectors having the reflective surface with the nanometer scale roughness.

Claim 6 (Original) The pixel of claim 5, wherein the plurality of reflectors are bent.

Claim 7 (Original) The pixel of claim 1, wherein the third structure comprises:

a color filter;

a scattering film between the color filter and the liquid crystal layer;

and

a polarizer above the color filter.

Claim 8 (Original) The pixel of claim 1, wherein the first structure further comprises a transparent region having a first area, and the second part comprises a second area of the reflective surface, with an area ratio of the first area to the second area ranged between 10% and 400%.

Claims 9 - 17 (Cancelled).

Claim 18 (Currently amended) A bottom plate for a pixel of an in-plane switching liquid crystal display, comprising:

a substrate;

a thin film transistor on the substrate;

a plurality of reflectors each including:

a micro scattering layer having a top surface with ~~the~~ a nanometer scale roughness defined by a height in the range of 5-50 nm and a width less than 20 nm, the micro scattering layer including a conductor and an insulator on the conductor, the insulator having a top surface with the nanometer scale roughness; and

a reflective layer on the micro scattering layer and conformal to the top surface to thereby form a reflective surface, wherein the reflective layer is made of a same metal of forming a gate of the thin film transistor;

a passivation covered on the plurality of reflectors; and

a conductor on the passivation and passing therethrough for connected to one of the plurality of reflectors.

Claims 19 - 21 (Cancelled).

Claim 22 (Original) The bottom plate of claim 18, further comprising a transparent region having a first area, and the plurality of reflectors having the top surface of a second area in total, with an area ratio of the first area to the second area ranged between 10% and 400%.